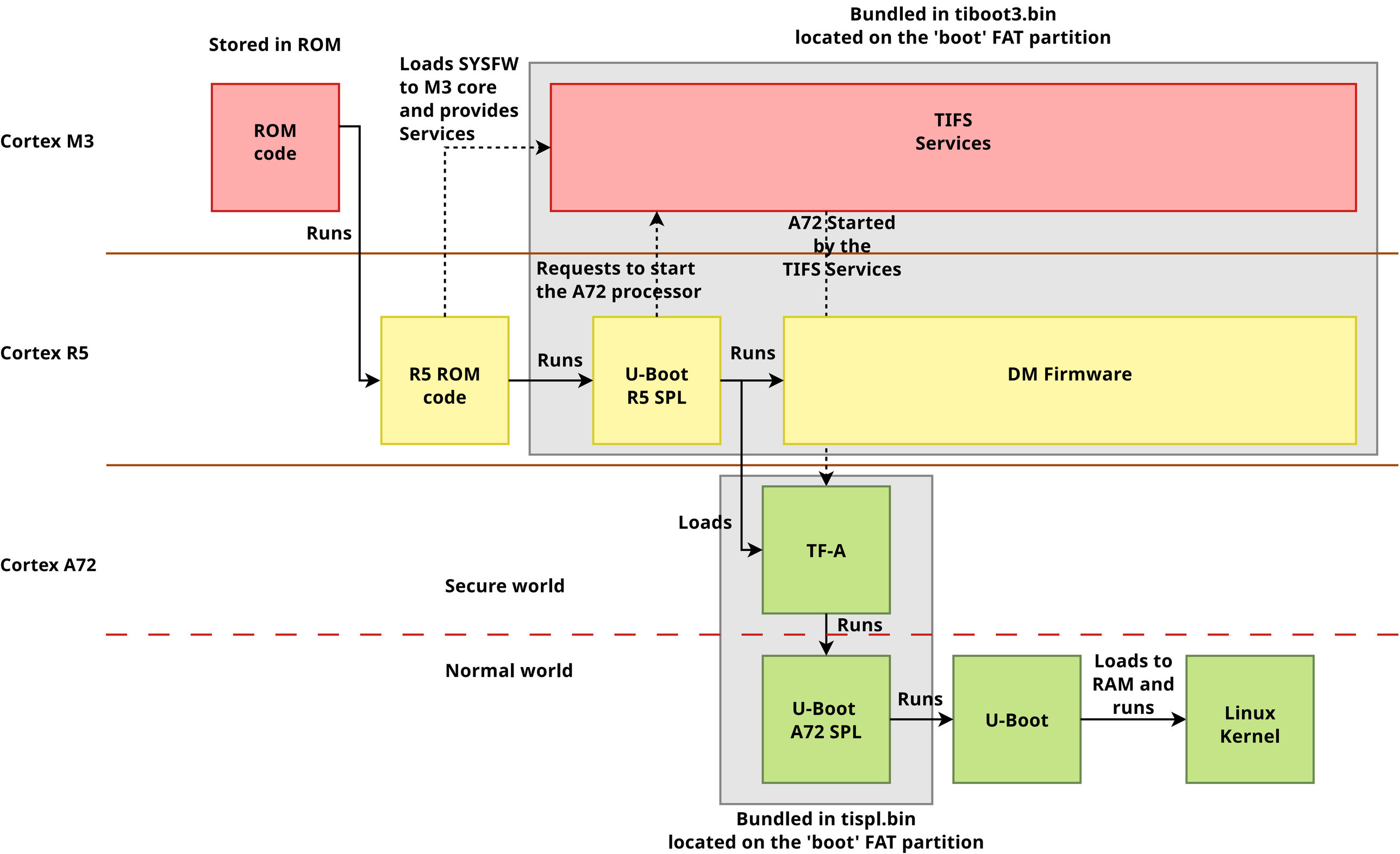
Bundled in tiboot3.bin
located on the 'boot' FAT partition
Stored in ROM
Loads SYSFW
to M3 core
and provides
Services
Cortex M3
ROM
code
TIFS
Services
Runs
A72 Started
by the
TIFS Services
Requests to start
the A72 processor
Cortex R5
R5 ROM
code
Runs
U-Boot
R5 SPL
Runs
DM Firmware
Loads
TF-A
Cortex A72
Secure world
Runs
Normal world
U-Boot
A72 SPL
Runs
U-Boot
Loads to
RAM and
runs
Linux
Kernel
Bundled in tispl.bin
located on the 'boot' FAT partition