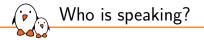


Discover and understand Embedded Linux

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Thomas Petazzoni

- ▶ Linux user since ≈1998
- Embedded Linux engineer since 2008
- CEO at Bootlin
- Co-maintainer of Buildroot
- 900+ patches in the Linux kernel
- Co-founder of Toulibre and Capitole du Libre

Bootlin

- Embedded Linux expertise
- Engineering and training
- ▶ 80% of business outside of France
- ► 20 people
- 8000+ patches in the Linux kernel
- Strong open-source culture
- Freely available training materials



- Most Linux users/developers are aware of Linux on desktops/servers
- But there's another field where Linux is very widely used: embedded systems!
- Goal of the talks
 - Show examples of embedded systems where Linux is used
 - HW architecture of Linux-based embedded systems
 - SW architecture of Linux-based embedded systems
- Ready?



An embedded system is a computer system—a combination of a computer processor, computer memory, and input/output peripheral devices—that has a dedicated function within a larger mechanical or electronic system



Increasing complexity / feature set

- 1. Purely analog electronics
- 2. Micro-controllers with bare-metal applications (no operating system) or relatively simple real-time operating systems
- 3. Micro-processors to run a rich operating system (Linux!) to provide multimedia features, connectivity, security, etc.
- Think about the evolution of TVs











Electronic equipment

















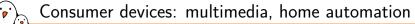


Structural monitoring





















Falcon 9 image from NASA, under public domain



Hardware

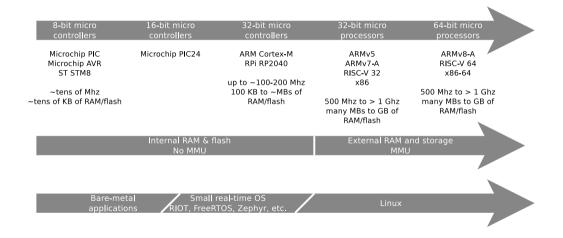
DDR3/DDR3L 1600@1.5V PCle Gen 3.0 with 16 Lanes (dual channel, ECC optionalwith up to Three Controllers 4th Generation SKU dependent) Intel[®] Core[™] or Intel[®] Pentium[®] **Display Ports** Processor Fully Integrated eDP/DP 1.2, DVI, HDMI* 1.4a Voltage Regulator (FIVR) Intel[®] Flexible Display DMI Interface (Intel[®] FDI) Analog VGA 4 to 6 SATA Ports Intel[®] H81 (2 to 4 SATA 6.0 Gbps) **Express Chipset** Intel[®] High Definition Audio Intel[®] 087/C226 14 Total USB Ports Chipset (4 to 6 USB 3.0) 6 to 8 PCle* x1 Gen 2.0 LPC or SPI Intel[®] Ethernet Connection I217-LM/I218-LM **BIOS Support**

Intel PC architecture

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CPUs in embedded systems



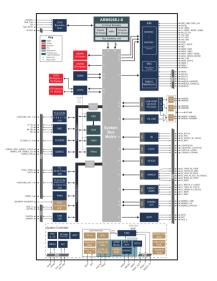


Concept of System-on-chip

Integrate many HW features in a single chip: system on chip

- One or several CPU cores
- Many HW interfaces
 - Slow: GPIO, UART, I2C, SPI
 - High-speed: MMC/SDIO, PCIe, USB
 - Multimedia: parallel in/out, DSI, CSI, LVDS, HDMI, I2S
- Accelerators: crypto, video encoding/decoding, GPU, NPU
- Silicon vendors design chips by combining hardware blocks
 - Purchased from IP vendors
 - Designed internally
- ▶ IP vendors: ARM, SiFive, Cadence, Synopsys, etc.
- Silicon vendors: NXP, TI, Microchip, Marvell, Rockchip, Qualcomm, etc.
- Massive number of SoCs available, to address very different markets

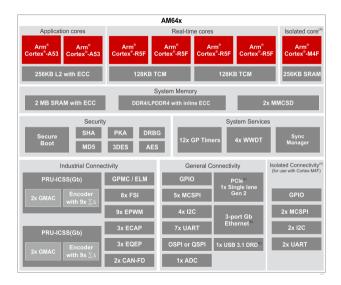






System Peripheral	stem Peripheral RK3588		Connectivity
Clock & Reset	Cortex-A76	Cortex-A55	USB OTG0 3.1/2.0/TypeC
PMU	Quad-Core	Quad-Core	USB OTG1 3.1/2.0/TypeC
18x PLL	(64K/64K L1 I/D Cache)	(32K/32K L1 I/D Cache)	2x USB Host 2.0
System register	2MB L2 Cache	512KB L2 Cache	SATA3/PCIe2.1/USB3Host
30x Timer		3MB L3 Cache	
16x PMW	Dual-clu	Dual-cluster Core	
5x Watchdog	MCU (Cortex-M0) x3		PCle3.0 (2x2,1x4,4x1)
2x Crypto			PDM/Audio PWM
SAR-ADC	High Performance NPU		4x I2S/PCM/TDM
TS-ADC	1MB Share Memory		2x SPDIF(8ch)
Interrupt Controller	Multi-Media Processor		10x UART
3x DMAC 6x PVTM	GPU Mali-G610 MP4	2D Graphics Engine	6x SPI
	(4x256KB L2 Cache)	an erofines argine	
3x Mailbox	Image Enhancement	Dual pipe ISP	9x 12C
Multi-Media	Processor	(Support camera HDR input)	2x Giga-Ethernet
Interface 2x MIPI-CSI DPHY 4L/CPHY 3L	8K Video Encoder (H265/H264)	8K 10-bits Video Decoder (H265/H264/VP9)	SDIO 3.0
4x MIPI-CSI DPHY 2L	JPEG Encoder/Decoder		GPIO
2x HDMI2.1 TX/eDP1.3 4 Lane 2x DP1.4 4 Lane with HDCP2.3	External Memory Interface		Embedded Memory
(Combo with USB3)	eMMC5.1	SD3.0/MMC4.5	SRAM
HDMI RX 2.0	LPDDR4/LPDDR4X/LPDDR5 Quad-channel x16bit		ROM
Display Controller (Support video HDR output)			OTP

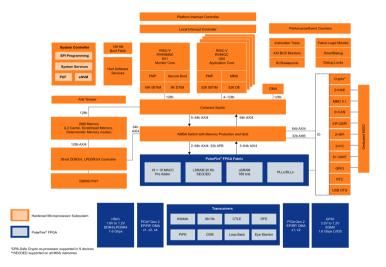
SoC example: Texas Instruments AM64



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SoC example: Microchip Polarfire

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Typical design of embedded hardware

- ▶ A custom *board* is typically designed for each embedded product
- A system-on-chip at its core
- RAM: DDR, LPDDR
- Storage: eMMC, SD, flash
- Display panel, sometimes a display bridge, a touchscreen controller
- Audio codec
- Camera sensor
- Ethernet PHY, Ethernet switch
- WiFi, Bluetooth chips, or other radio interfaces
- Power supplies, protections
- Connectors



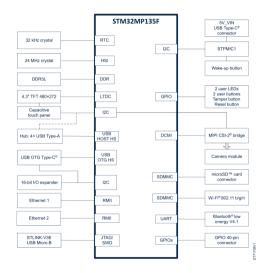




Board example: Amazon Ring



Board block diagram: STM32MP135 evaluation board



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In Embedded Linux systems, Linux runs on application processors

- Some systems have special requirements
 - Very tight real-time requirements
 - Safety requirements
 - Support of custom hardware interfaces
- Integration of
 - Micro-controllers: to run bare-metal code / small real-time OS
 - FPGA: to program custom hardware logic
- … either …
 - Inside the system-on-chip
 - or on the board



Software

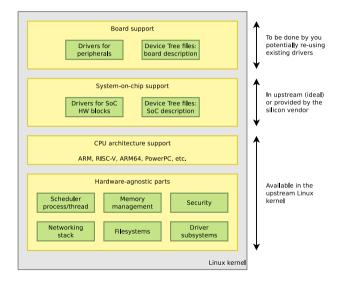






- Making sure that the hardware is supported by the software
- Mostly affects the bootloader and Linux kernel
- ► Goal is to have all required hardware interfaces/features supported
- Requires
 - Porting/adaptation
 - Sometimes new drivers
- The hardware in each system-on-chip is often different: different drivers are required
- ▶ The hardware in each board is often different: different drivers are required
- There is no such thing as "a kernel that works on ARM"







- Ideally: the upstream Linux kernel, and an upstream bootloader has support for your SoC, and one or several evaluation boards
- Less ideally: your silicon vendor provides a fork of Linux + bootloader that includes support for your SoC, and one or several evaluation boards
 - Less ideal because: generally not maintained over the long run, poor quality, non-standard interfaces
- Even less ideally: what your silicon vendor provides is so crappy that you can't use it
 - More work/effort for you



- Mainly: what is specific to the hardware on your board
- > Your hardware often derived from a reference design/evaluation board
- Bootloader level
 - DDR controller configuration, potentially a tricky part
 - UART
 - Storage: eMMC, SD, NAND flash
 - Sometimes networking, or other peripherals depending on the use cases
 - Always: Device Tree (description of HW) + configuration
 - Sometimes: new drivers needed for on-board peripherals
- Linux kernel level
 - All hardware features that are needed
 - Always: Device Tree (description of HW) + configuration
 - Sometimes: new drivers needed for on-board peripherals



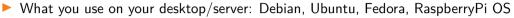
- ▶ On x86
 - UEFI firmware \rightarrow GRUB bootloader \rightarrow Linux kernel
- Most ARM 32-bit platforms
 - ROM code \rightarrow U-Boot bootloader \rightarrow Linux kernel
- ARM 64-bit platforms (simplified)
 - ROM code \rightarrow Trusted Firmware \rightarrow U-Boot bootloader \rightarrow Linux kernel
 - Sometimes a trusted operating system, such as OP-TEE
 - Sometimes additional firmware, running on co-processors
- Bootloader is generally *U-Boot*, not *Grub*
- Exact boot flow is specific to the system-on-chip



System integration

How do you integrate all the software components together?

- Bootloader
- Linux kernel
- Open-source components: systemd? Wayland? GStreamer? NetworkManager? Python? NodeJS? Qt? Gtk? OpenCV? etc.
- In-house components: your libraries/services/applications
- Varying levels of complexity
 - Some embedded Linux systems have a very simple software stack
 - Some need very complex software stacks
 - And everything in-between
- Two main options
 - Binary distributions
 - Embedded Linux build systems

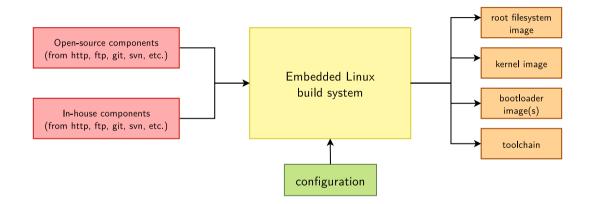


- Provides
 - Pre-compiled packages of many open-source software components
 - Installer

Binary distribution

- Configuration/integration
- Good:
 - Easy to install, easy to install packages, well known
 - Regular updates, including security
- Less good for embedded:
 - Biased towards installation rather than factory install a pre-configured image
 - Biased towards native compilation, and development on the target
 - Difficult to have a reproducible image creation process
 - Large, not easy to tweak/optimize, lots of mandatory dependencies







Embedded Linux build system

- Most popular build systems:
 - *Yocto*, based on *OpenEmbedded*: very powerful, flexible, smart, but complex, steep learning curve
 - Buildroot: simpler, but less flexible and somewhat "dumb"
- Everything is rebuilt from source
- Good:
 - Flexibility in the tuning/optimization
 - Reproducibility
 - Smaller footprint: less packages, reduced dependencies
 - · Generates an image to flash, not an installer
 - · Cross-compilation: fast build machine, no development files on target
- Less good:
 - Need to learn (but fun!)
 - Build time
 - Security updates good, but perhaps not as strong as Debian



Factory flashing and OTA

Factory flashing

- Linux on embedded devices is not installed by users
- Devices are flashed at the factory with an image of the Linux system
- Image must be fully functional with all software installed and configured
- Provisioning of MAC address, serial number, keys, etc.
- The user doesn't even notice there's Linux underneath

Over-the-air updates

- Old trend: ship device with a firmware, never touch it again
- Now, devices must be updated
- Fix bugs, security issues, deploy new features
- Generally: image-based update, not package-based
- Two read-only copies of the system + one data partition
- Popular OTA solutions: RAUC, swupdate, Mender



Cyber-security regulations more and more strict

Defensive security

- Secure boot: authenticate all software running on the device, only boot/run software signed by the manufacturer
- Encryption: protect the OS/application and/or the user data/configuration
- Containers, virtualization
- Mandatory access control: SELinux, AppArmor, etc.
- Updates to fix vulnerabilities
 - Monitor CVEs
 - Long-term support in Linux kernel, Yocto, Buildroot
 - Deploy updates in the field
 - How often? Impact on testing, validation, certification?



- An embedded Linux system is just a normal Linux system, with usually a smaller selection of components
- In terms of application development, developing on embedded Linux is exactly the same as developing on a desktop Linux system
- > All existing skills can be re-used, without any particular adaptation
- All languages, frameworks, libraries, can be integrated into the embedded Linux system
 - Python, Rust, C++, Go, NodeJS, etc.
 - Beware of the limits of your embedded hardware in terms of performance, storage and memory

Want to get started?

- Buy a hardware platform
 - NOT a RaspberryPi!
 - BeagleBoard: BeaglePlay (ARM64), Beagle-V (RISC-V)
 - STM32MP1 discovery kits
 - Plenty of inexpensive boards based on Allwinner or Rockchip processors
- Build your embedded Linux system
 - Configure/build your bootloader
 - Configure/build your kernel
 - Build a custom system with Yocto/Buildroot
 - Explore hardware interfaces
- Don't use vendor solutions/tools: use upstream Linux, upstream U-Boot, upstream Yocto/Buildroot
- Bootlin training materials available free of charge, including practical labs!



We're hiring:

Internships on embedded Linux topics

Embedded Linux/Linux kernel engineers

Toulouse, Lyon, remote

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Questions?