

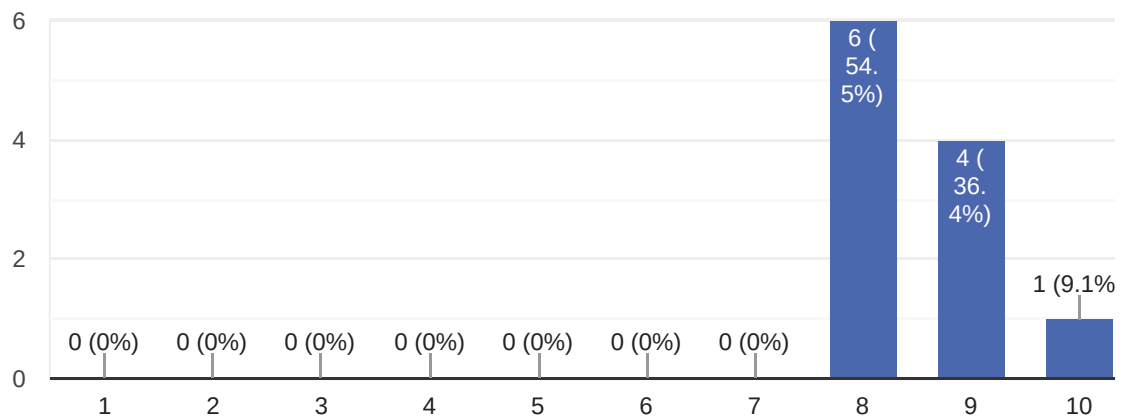
# Bootlin training course evaluation

11 responses

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## Overall rating of the course

11 responses



## Comments and suggestions

6 responses

I found the quiz to be a little hard as it takes a while to digest the concepts. I also think it would be good to somehow incorporate actually doing the labs during the training or creating groups that could work together outside of the class.

Presentation was cool, and explication. I love documentations (Slides,lab slides). I'll use it

Definitely equipped with more knowledge than before the training.

Really comprehensive and good coverage of the selected topics.

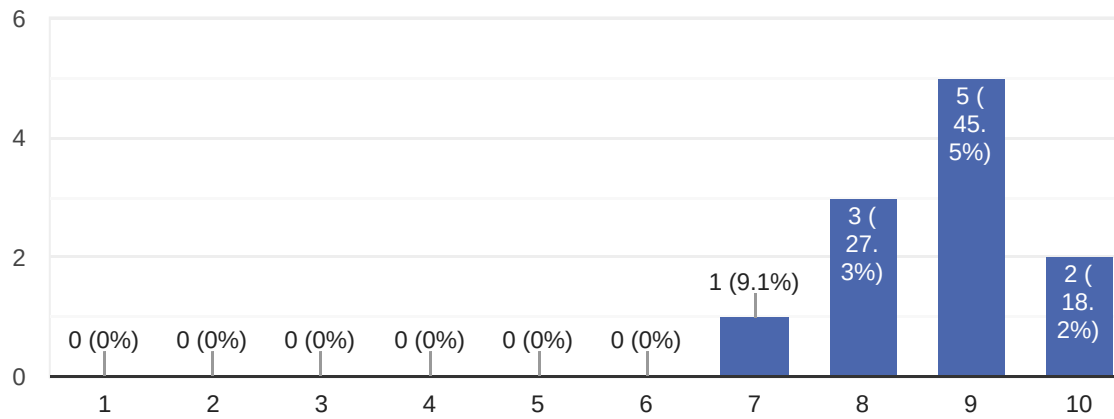
I had a little trouble understanding the lecturer speaking.

I would prefer to use a virtual machine or some other way to not having to install a lot of stuff and make configuration changes to my physical PC



### How useful were the lectures?

11 responses



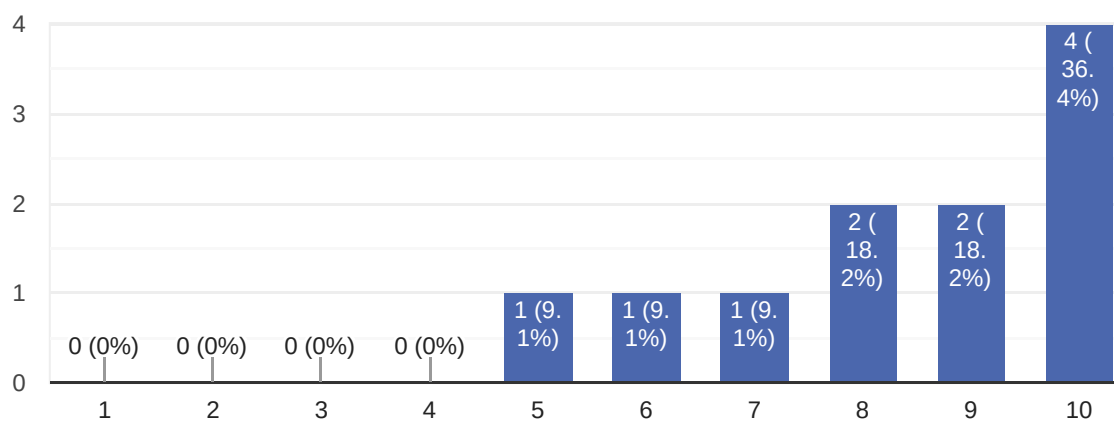
### Comments and suggestions

1 response

As you covered a lot of topics I understand that you couldn't go deep to every detail.

### How useful were the practical demos?

11 responses



## Comments and suggestions

5 responses

It's much more important for me to do these but I'm afraid it will be much harder without support when I do.

It could be nice to practice on virtual Machine or on Qemu

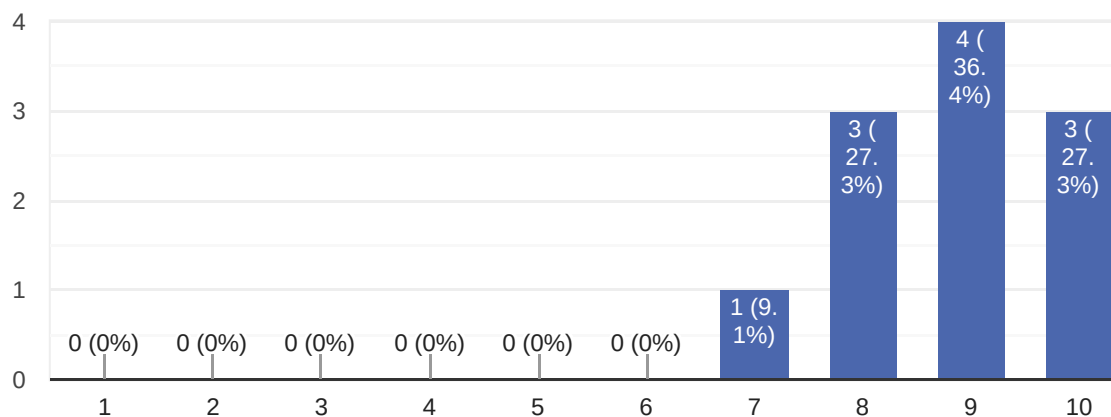
I think demo is most important part, giving that we cannot attend training in person.

Connecting theory and practice is always good. the practical demos are a good break in the lectures.

IMO there should be prepared detailed solutions listing every step of the demo to be used by the participants when they get stuck trying to do the labs themselves. We got solutions by Greg each day, but this could have been prepared in advance.

## How would you rate the overall organization of the course?

11 responses



## Comments and suggestions

2 responses

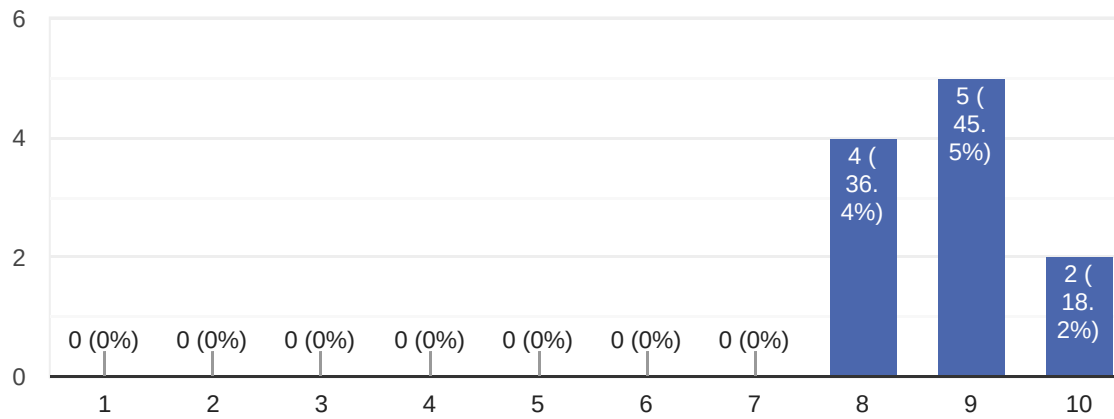
I think more interesting topics has less content in the course like kernel memory model, locking, DMA, mmap etc.

It is a lot of information to digest!



### How would you rate the trainer?

11 responses



### Comments and suggestions

4 responses

He is great and explains things clearly.

Knowledgeable and eager to explain when people had questions. It took more energy to understand sometimes because of the french accent.

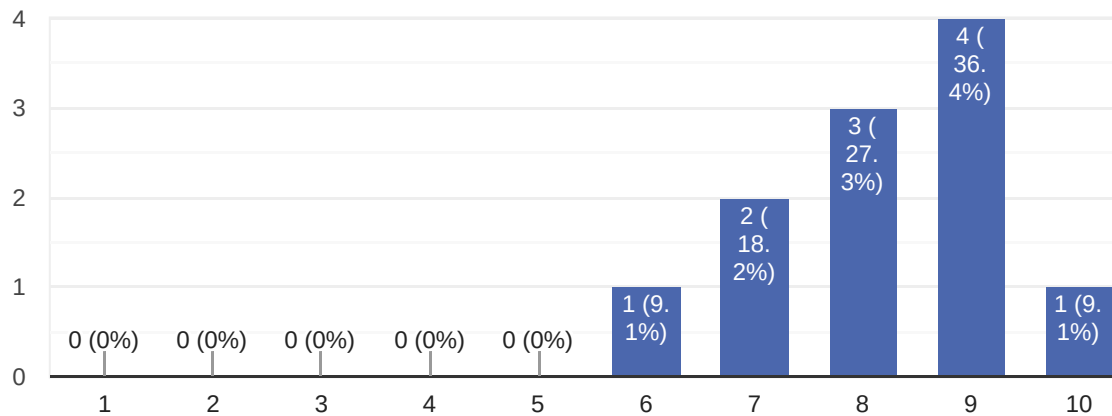
Grégory knows his stuff. Very eager to find an answer to the questions if he does not have it already.

Very knowledgeable :-)



### How did the course meet your learning objectives?

11 responses



### Comments and suggestions

2 responses

Wished for more time for more topics coverage on backup slides.

Offering 1 kernel course means you have to try to cover a wide range. I would have preferred more focus on advanced topics (DMA, interrupts), so maybe 2 courses would be something to consider? ARM32 is getting a bit dated...



What part(s) of the course did you like most?

7 responses

Information and resource link

UART, Device Tree

All, I really learn many things, I'll reread this document for better understanding

I think the various demos with progressing driver. Especially the UART serial driver demo up till the debugging pieces.

Building a driver from scratch. Backing up the contents of the slides with a demo make i.e the structure linking more tangible.

Demos

I liked the entire course.

What part(s) of the course did you like least?

2 responses

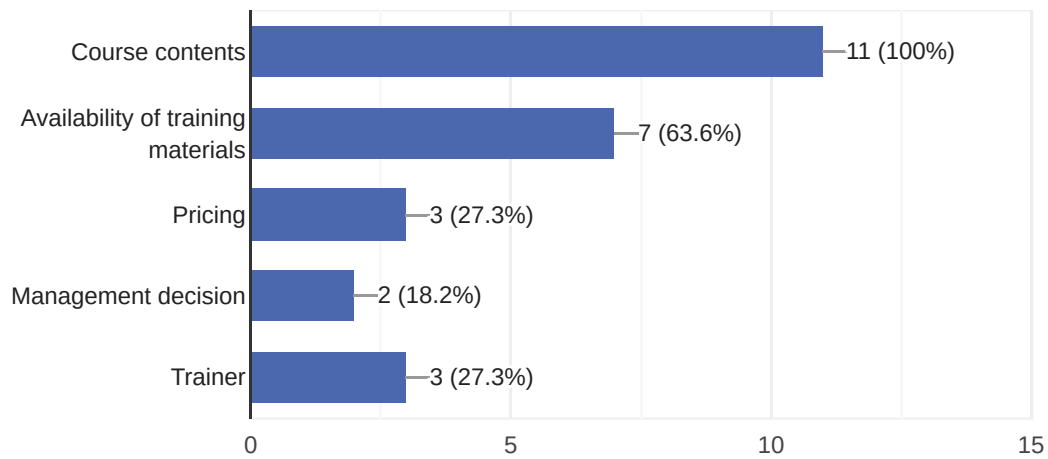
Expected more in-depth coverage on kernel concepts like memory[pages, mm\_struct etc, interrupts, smp handling code] with a processor as an example

Diagrams could be more understandable and overall effective.



### What reasons prompted you to choose a Bootlin course?

11 responses



### Comments

4 responses

Maybe consider a separate training for advanced Linux developers on kernel internals and sub-systems

Like your opensource stance and able to review materials beforehand

A good informative course. It points out what I need to learn more about.

Really glad I was able to attend.



### Further training needs?

5 responses

sysfs, basic buildroot, bootlin/ linux for xilinx FPGA

Imaging Sensors and V4L Subsystem

YOCTO

More advanced driver development with USB, PCI systems

Kernel internals, advanced driver development.

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