




## Embedded Linux Security

Course duration \_\_\_\_\_

 4 half days – 16 hours

Language \_\_\_\_\_

Materials English


Oral Lecture English  
French


Trainer \_\_\_\_\_

One of the following engineers

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### Audience

Companies and engineers who design, develop and produce embedded Linux systems that have cyber-security requirements.

### Training objectives

- Be able to understand the security and isolation mechanisms of modern embedded Linux systems: non-executable memory, address space randomization, privilege levels, mandatory and discretionary access control.
- Be able to design and implement a secure boot chain, from the bootloader all the way to userspace, including dm-verity.
- Be able to reason about cryptography, and implement secure key storage, using hardware or software HSM.
- Be able to leverage ARM TrustZone technology (secure world, TF-A, OP-TEE) to isolate sensitive operations and develop Trusted Applications.
- Be able to implement userland security mechanisms: Linux capabilities, namespaces, cgroups, SECCOMP, SELinux, and systemd hardening features.
- Be able to set up filesystem encryption using dm-crypt and integrate it with secure key management through PKCS#11.
- Be able to manage software vulnerabilities using CVE databases, generate and analyze Software Bills of Materials (SBoM), and understand compliance requirements including the Cyber Resilience Act.
- Be able to design and deploy secure update mechanisms using A/B partitioning schemes and tools like RAUC or SWUpdate.
- Be able to understand measured boot concepts and implement platform integrity verification using TPMs and IMA/EVM.

### Prerequisites

- **Knowledge and practice of UNIX or GNU/Linux commands:** participants must be familiar with the Linux command line. Participants lacking experience on this topic should get trained by themselves, for example with our freely available on-line slides.
- **Minimal experience in embedded Linux development:** participants should have a minimal understanding of the architecture of embedded Linux systems: role of the Linux kernel vs. user-space, development of Linux user-space applications in C. Following [Bootlin's Embedded Linux course](#) allows to fulfill this pre-requisite.
- **Minimal English language level: B1**, according to the *Common European Framework of References for Languages*, for our sessions in English. See the [CEFR grid](#) for self-evaluation.

### Pedagogics

- Lectures delivered by the trainer, over video-conference. Participants can ask questions at any time.
- Practical demonstrations done by the trainer, based on practical labs, over video-conference. Participants can ask questions at any time. Optionally, participants who have access to the hardware accessories can reproduce the practical labs by themselves.
- Instant messaging for questions between sessions (replies under 24h, outside of week-ends and bank holidays).
- Electronic copies of presentations, lab instructions and data files. They are freely available [here](#).

### Certificate

Only the participants who have attended all training sessions, and who have scored over 50% of correct answers at the final evaluation will receive a training certificate from Bootlin.

### Disabilities

Participants with disabilities who have special needs are invited to contact us at [training@bootlin.com](mailto:training@bootlin.com) to discuss adaptations to the training course.



Online seminar

## Required equipment

Mandatory equipment:

- Computer with the operating system of your choice, with the Google Chrome or Chromium browser for videoconferencing.
- Webcam and microphone (preferably from an audio headset).
- High speed access to the Internet.

Optionnally, if the participants want to be able to reproduce the practical labs by themselves, they must separately purchase the hardware platform and accessories, and must have a PC computer with a native installation of Ubuntu Linux 24.04.

## Hardware platform for practical labs

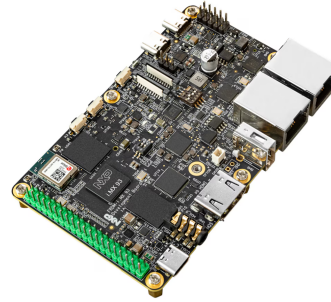
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### NXP i.MX93 FRDM

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**NXP FRDM-IMX93** development board

- NXP i.MX93 SoC (Dual Cortex-A55 + Cortex-M33)
- 2 GB LPDDR4X, 32 GB eMMC
- Dual Gigabit Ethernet
- USB 2.0 Type-C + USB Type-A
- CAN interface
- MicroSD slot, EEPROM
- Wi-Fi 6 + Bluetooth 5.4 + 802.15.4 (MAYA-W276)
- HDMI output (via LVDS), MIPI DSI and CSI
- Audio jack (MQS), buttons and LEDs
- SWD and UART debug



## Half day 1

Lecture	Fundamental concepts	<ul style="list-style-type: none"> <li>Threat modeling</li> <li>Cryptography basics: public/private key</li> <li>Understanding a TLS handshake from start to finish</li> <li>Understanding Public Key Infrastructures</li> </ul>
Demo	Setting up a small PKI	<ul style="list-style-type: none"> <li>Using OpenSSL to generate a key hierarchy</li> <li>Experimenting with key revocation</li> <li>Performance comparison of public/private key encryption</li> </ul>
Lecture	Hardware-enforced security barriers	<ul style="list-style-type: none"> <li>General security concepts: kernel/user split, CPL, memory protections</li> <li>Common mitigations: ASLR, NX/DEP, SMAP/SMEP, RELRO</li> <li>ARM features: Exception levels</li> <li>ARM features: Secure world, TF-A, OP-TEE</li> <li>Understanding a full ARMv8 bootflow, including secure world</li> <li>Understanding Trusted Applications</li> </ul>
Demo	Exploring the secure world	<ul style="list-style-type: none"> <li>Building secure world software (TF-A, OP-TEE, TA)</li> <li>Adding logs to observe Exception level transitions</li> <li>Adding logs to observe world transitions</li> <li>Writing a small Trusted Application in OP-TEE</li> <li>Interacting with our Trusted Application from userland</li> </ul>

## Half day 2

Lecture	Secure boot: part 1	<ul style="list-style-type: none"> <li>Understanding the secure boot chain concept</li> <li>Secure boot implementation examples: x86 and ARM SoCs</li> <li>Secure boot: threat model</li> <li>Be able to design and implement a secure boot chain</li> </ul>
Demo	Secure boot on i.MX93: ROM stage	<ul style="list-style-type: none"> <li>Building all software components for secure boot (AHAB) on i.MX93</li> <li>Creating a signed AHAB container using SPSDK</li> <li>Flashing the fuses on the i.MX93 to enable secure boot (AHAB)</li> <li>Testing that secure boot (AHAB) is correctly enabled</li> </ul>
Lecture	Secure boot: part 2	<ul style="list-style-type: none"> <li>Deep dive into the U-Boot components: TPL/SPL, U-Boot proper, FIT images</li> <li>Hardware description: Device Tree</li> <li>Enabling signature verification in the U-Boot bootloader</li> <li>RootFS verification: <i>dm-verity</i> and implications for system design</li> </ul>
Demo	Secure boot on i.MX93: boot-loader stage	<ul style="list-style-type: none"> <li>Integrating the public key into the DTB</li> <li>Adding signature to the kernel FIT image</li> <li>Configuring U-Boot to enforce signature verification</li> <li>(optional) Configuring SPL to also enforce signature verification</li> </ul>

## Half day 3

Lecture	Confidential information	<ul style="list-style-type: none"> <li>Filesystem encryption, presentation of dm-crypt/cryptsetup</li> <li>Key management: Hardware Security Module</li> <li>Secure key usage: Introduction to PKCS#11</li> <li>Secure key usage: kernel keyring and trusted keys</li> </ul>
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Demo	Secure key management	<ul style="list-style-type: none"> <li>▪ Provisioning keys into the i.MX93 ELE</li> <li>▪ Using OP-TEE as software HSM</li> <li>▪ (optional) Filesystem encryption using the ELE key</li> <li>▪ (optional) Signing U-Boot FITs with HSM integration over PKCS#11</li> </ul>
Lecture	Userland security measures	<ul style="list-style-type: none"> <li>▪ Access Control paradigms: MAC/DAC</li> <li>▪ Linux capabilities: usage and examples</li> <li>▪ Process isolation: Namespaces, Cgroups, SECCOMP</li> <li>▪ Linux Security Modules: SELinux, AppArmor</li> <li>▪ Application hardening via <i>systemd</i></li> </ul>
Demo	Restricting userland applications	<ul style="list-style-type: none"> <li>▪ Preventing a simple application from executing shellcode using SECCOMP</li> <li>▪ Manipulating SELinux contexts</li> <li>▪ Using <i>systemd</i> to restrict a daemon's access to resources</li> </ul>

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### Half day 4

Lecture	Maintenance, regulations and compliance	<ul style="list-style-type: none"> <li>▪ Introduction to vulnerability frameworks: CVE, CWE, CVSS</li> <li>▪ A walkthrough of the Cyber Resilience Act (CRA)</li> <li>▪ Defining an upgrade strategy, based on the release and maintenance cycles of important open-source projects</li> <li>▪ Generating a Software Bill of Materials</li> <li>▪ Analyzing a Software Bill of Materials</li> </ul>
Demo	Getting familiar with SBoMs and CVEs	<ul style="list-style-type: none"> <li>▪ Generating an SBoM for a Yocto distribution</li> <li>▪ Analyzing the generated SBoM using <i>sbom-cve-check</i></li> <li>▪ Patching a CVE present in the SBoM</li> <li>▪ Differential analysis</li> </ul>
Lecture	Secure updates	<ul style="list-style-type: none"> <li>▪ A/B updates</li> <li>▪ Presentation of SWUpdate</li> <li>▪ Presentation of RAUC</li> <li>▪ Bootloader integration</li> <li>▪ Consequences on the secure boot chain</li> </ul>
Demo	Setting up RAUC for secure updates	<ul style="list-style-type: none"> <li>▪ RAUC configuration on the target</li> <li>▪ Building, shipping and installing a RAUC bundle</li> <li>▪ Bundle signature verification using PKCS#11</li> <li>▪ (optional) RAUC bundle encryption</li> <li>▪ (bonus) HSM integration</li> </ul>