Wii Nunchuk Interface

The Wii Nunchuk uses a proprietary connector with six pins. The connections are as follows.

1 3 5 2 4 6	1 = +3V (3V recommended but works at 5V) 3 = N/A 5 = Data	2 = Clock 4 = N/A 6 = Gnd
-------------	---	---------------------------------

Figure 1: Wii Nunchuk Pinout

To communicate with the Nunchuk, we must send a handshake signal. If you are using a **black** Wii Nunchuk, send 2 bytes $0 \times F0$, 0×55 to initialize the first register and $0 \times FB$, 0×00 to initialize the second register of the Nunchuk. On a **white** Wii Nunchuk, send 0×40 , 0×00 followed by 0×00 . The I²C address of both Wii Nunchuks is 0×52 . The frequency used to communicate with the Wii Nunchuk is $100 \, \text{KHz}$.

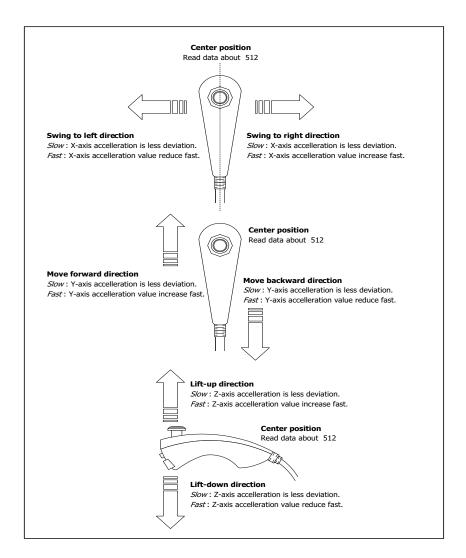


Figure 2: Wii Nunchuk Physical Operation

I²C Bus: Inter-Integrated Circuits

The I²C (Inter-IC) bus is a bi-directional two-wire serial bus that provides a communication link between integrated circuits (ICs). Phillips introduced the I²C bus 20 years ago for mass-produced items such as televisions, VCRs, and audio equipment. Today, I²C is the de-facto solution for embedded applications. It's also known as the "two-wire interface" (TWI), due to the fact that it uses just two wires: SDA (serial data) and SCL (serial clock). A typical I²C configuration consists of one master device and any number of slave devices sharing the bus. The master initiates all transactions with the slaves. In this lab, the Intel Atom motherboard acts as the master device and the Wii Nunchuk is the slave device.

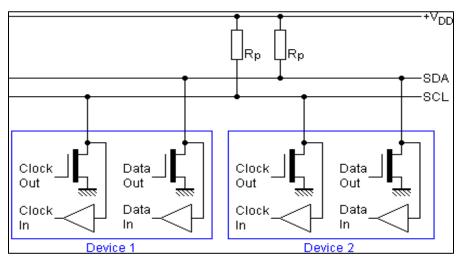


Figure 3: I²C Device Circuit

Another name you will hear used in connection with I^2C is SMBus (System Management Bus). The SMBus protocol was developed by Intel in 2005 and is based on I^2C . SMBus defines a stricter range of operating limits, including clock speed, timing and data formats, compared to I^2C . Many I^2C devices have, in turn, incorporated policies from SMBus and both kinds of devices can often be used on the same bus.

Wii Nunchuk Output

The Wii Nunchuk is a slave I^2C bus device that outputs six bytes of data as follows.

Data byte receive Joystick X Joystick Y Accelerometer X (bit 9 to bit 2 for 10-bit resolution)																	
											elerometer \						0:
									Accelerometer Z (bit 9 to bit 2 for 10-bit resolution)								0:
									Accel. Z bit 1	Accel. Z bit 0	Accel. Y bit 1	Accel. Y bit 0	Accel. X bit 1	Accel. X bit 0	C-button	Z-button	02
Byte	0x00 :	X-axis data	of the joy	rstick													
,																	
вуте	0x01:	Y-axis data	or the Joy	STICK													
Byte	0x02:	X-axis data	of the ac	cellerome [.]	ter sensor												
Byte	0x03:	Y-axis data	of the ac	cellerome	ter sensor												
Byte	0x04:	Z-axis data	of the ac	celleromet	er sensor												
Byte	0x05 :	bit 0 as Z b	utton statu	us - 0 = pre:	ssed and 1	= release											
		bit 1 as C b	outton stat	rus - 0 = pre	essed and	1 = release)										
		bit 2 and 3	as 2 lowe	er bit of X-a	xis data of	the accel	lerometer :	senso									
		bit 4 and 5	as 2 lowe	er bit of Y-a	xis data of	the accel	lerometer s	senso									
		bit 6 and 7	as 2 lowe	r bit of Z-ax	kis data of	the accell	erometer s	enso									

Figure 3: Wii Nunchuk I²C Output

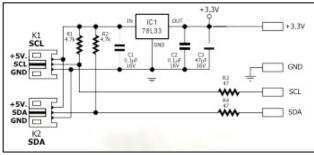


Figure 4: Nunchuk Interface Board Circuit